

REMARKS:

Figures 1, 1A, 2, 5, 6 and 7 of the drawings are amended. Replacement figures are presented which incorporate the desired changes and comply with 37 CFR 1.84. An explanation of the changes is presented in the remarks section of this amendment and is accompanied by a marked-up copy of the figures being amended, with annotations.

The specification has been amended to correct the characterization of transistors Q5 and Q6; they are not in a current mirror configuration and this is supported in Figures 2 and 5 as filed. The specification has also been amended to correct a typing error.

Claims 1 and 14 has been amended to state that the output signals of the output stage have the same current without stating that this is caused by the intermediate gain stage. Review of the figures as originally filed reveals that the output signals have the same current and this is caused by Q5 and Q6 sharing common base and emitter terminals. This amendment is supported by the specification and figures as filed.

FIGs. 1, 1A, 2, 5 and 7 have been amended to show the proper symbol for Vcc. FIGs. 2, 5, and 7 have been amended to show the proper symbol for Vss. FIGs. 2, 5 and 7 have been amended to correct the direction of the current source symbols. FIG. 2 has been amended to show the proper input to transistor Q1. FIG. 6 has been amended to shown the proper input line to the negative terminal of current cell 10. FIG. 7 has been amended to show a connection dot and to connect current sources I1, I2, and I3 all to Vss. These changes are supported by the specification as filed.

This amendment corrects drawing and specification problems noted during a review of the allowed application. No new matter has been added. An office action is earnestly solicited at the Examiner's earliest convenience.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read "Renee' Michelle Larson", is written over a horizontal line.

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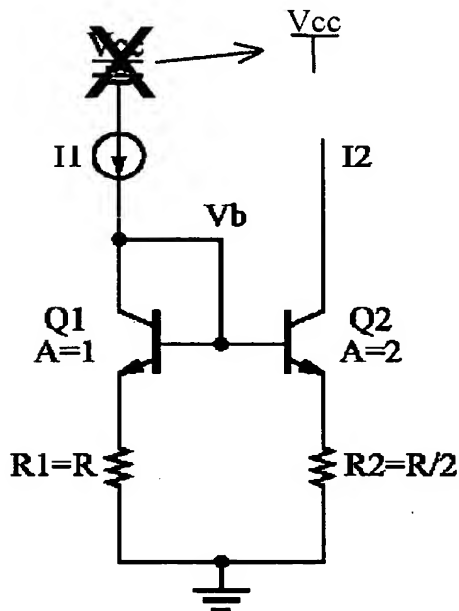


Figure 1
Prior Art - Current Amplifier

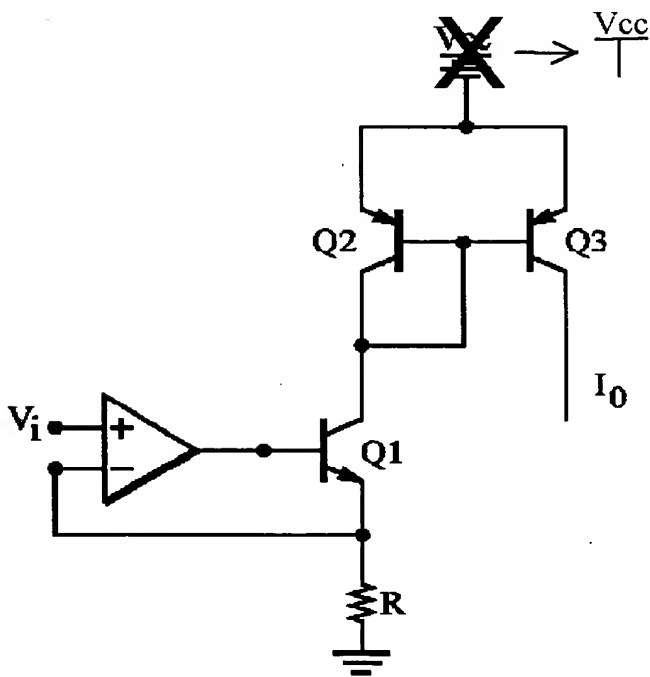


Figure 1A
Prior Art - Voltage to Current Converter

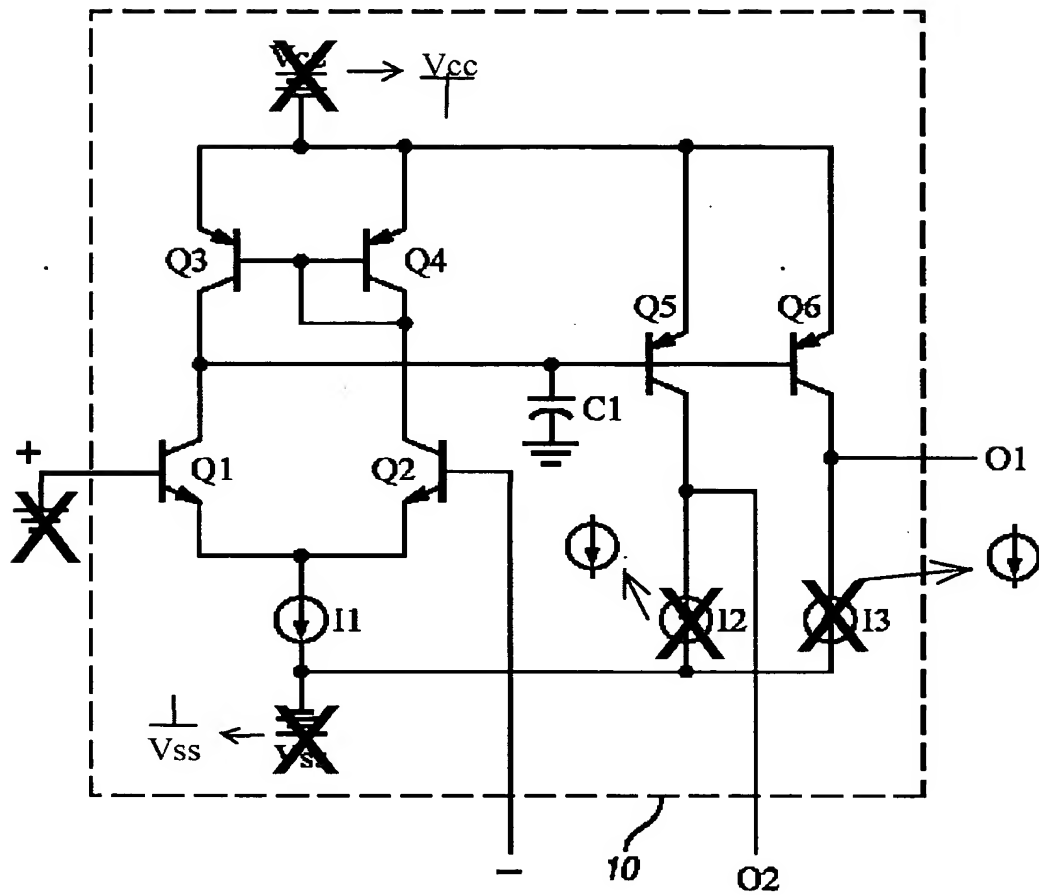


Figure 2

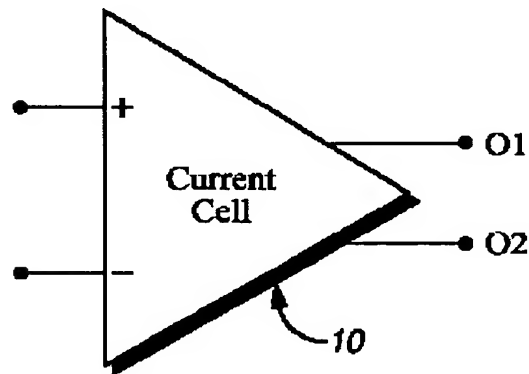
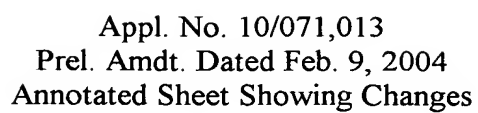


Figure 3



The diagram shows a triangular component labeled 'Current Cell' with a reference numeral '10'. The non-inverting input (+) is connected to ground. The inverting input (-) is connected to a current source I_{in} and a resistor $R1$. The output terminal $O2$ is connected to resistor $R1$ and resistor $R2$, which is grounded. The output terminal $O1$ is also shown.

Figure 5

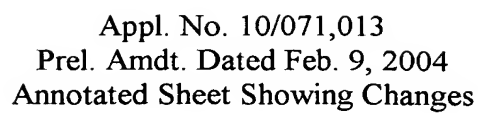


Figure 7